



PATENT APPLICATION  
Docket No. 11675.99/2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Phillip J. Ireland et al.

Serial No.: 10/034,236

Filed: December 28, 2001

For: METHOD OF MAKING A CONTACT STRUCTURE

Examiner: H. Jey Tsai

Confirmation No.: 9992

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) Art Unit  
) 2812  
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CERTIFICATE OF DEPOSIT UNDER 37 C.F.R. § 1.8

I hereby certify that the following documents are being deposited with the United States Postal Service as First Class Mail, postage prepaid, in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 3<sup>rd</sup> day of July 2003.

- Transmittal of Issue Fee
- Issue Fee Transmittal Form PTOL-85B
- "Fee Address" Indication Form PTO/SB/47
- Comments on Statement of Reason for Allowance
- Credit Card Payment Form PTO-2038 for \$1,624.00
- Postcard

Respectfully submitted,

Gregory M. Taylor  
Attorney for Applicants  
Registration No. 34,263



**022901**

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Art Unit  
2812

TRANSMITTAL OF ISSUE FEE

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Sir:

In response to the Notice of Allowance and Issue Fee Due dated April 11, 2003, please find enclosed herewith the following items for filing in the United States Patent and Trademark Office in connection with the above identified patent application.

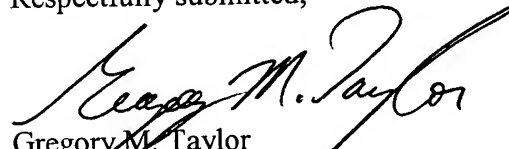
- X   Issue Fee Transmittal (PTOL-85) submitted pursuant to 37 C.F.R. § 1.311
- X   “Fee Address” Indication Form PTO/SB/47

- x Comments on Statement of Reason for Allowance
- x A Certificate of Deposit Under 37 C.F.R. § 1.8
- x Credit Card Payment Form PTO-2038 authorizing payment of \$1,624.00 to cover:
  - x The \$1,300.00 government issue fee pursuant to 37 C.F.R. § 1.18
  - x \$300.00 publication fee
  - x \$24.00 for eight (8) copies of the patent when issued.
- x The Commissioner is hereby authorized to charge payment of any additional fees or credit any overpayment to Deposit Account No. 23-3178. A duplicate copy of this letter is enclosed.

Please address all future correspondence in connection with the above-identified patent application to the attention of the undersigned.

Dated this 3<sup>rd</sup> day of July 2003.

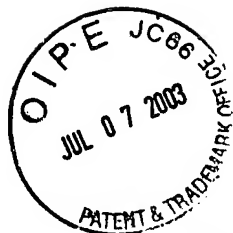
Respectfully submitted,

  
Gregory M. Taylor  
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Registration No. 34,263



**022901**

PATENT TRADEMARK OFFICE



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Examiner: H. Jey Tsai )  
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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

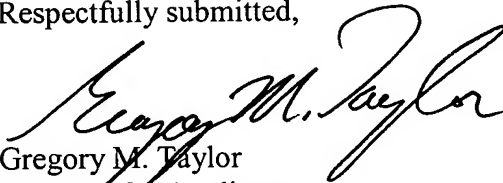
In response to the Notice of Allowance and Issue Fee Due dated April 11, 2003, Applicants have the following comments on the statement of reasons for allowance in the above-identified patent application.

The Examiner stated that claims 1-43 recite "the combination of etching the sleeve insulator layer from the bottom surface within the lower bulk insulator layer above the semiconductor substrate, from the top surface of the upper bulk insulator layer, such that the sleeve insulator layer has a terminus at an interface between the upper bulk insulator layer and the cell plate insulator layer and extends to an opposite terminus that is above the semiconductor substrate, within the lower bulk insulator layer and below the capacitor dielectric layer."

Applicants note that all of these limitations are only recited in independent claims 7, 11, and 12, and are not all recited in independent claims 1, 13, 20, 27, 32, 37, and 43. Nevertheless, all of the allowed claims still present patentable subject matter.

Dated this 3<sup>rd</sup> day of July 2003.

Respectfully submitted,



Gregory M. Taylor  
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